

FIG. 1

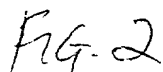
[illegible]

Figure 5. Input PID Table

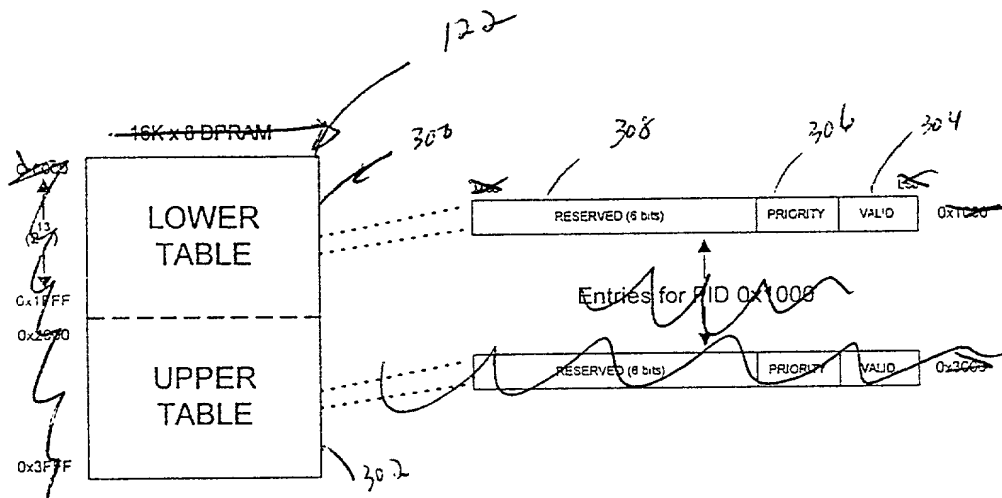


FIG. 3



Figure 10: Simplified ISR Flowchart

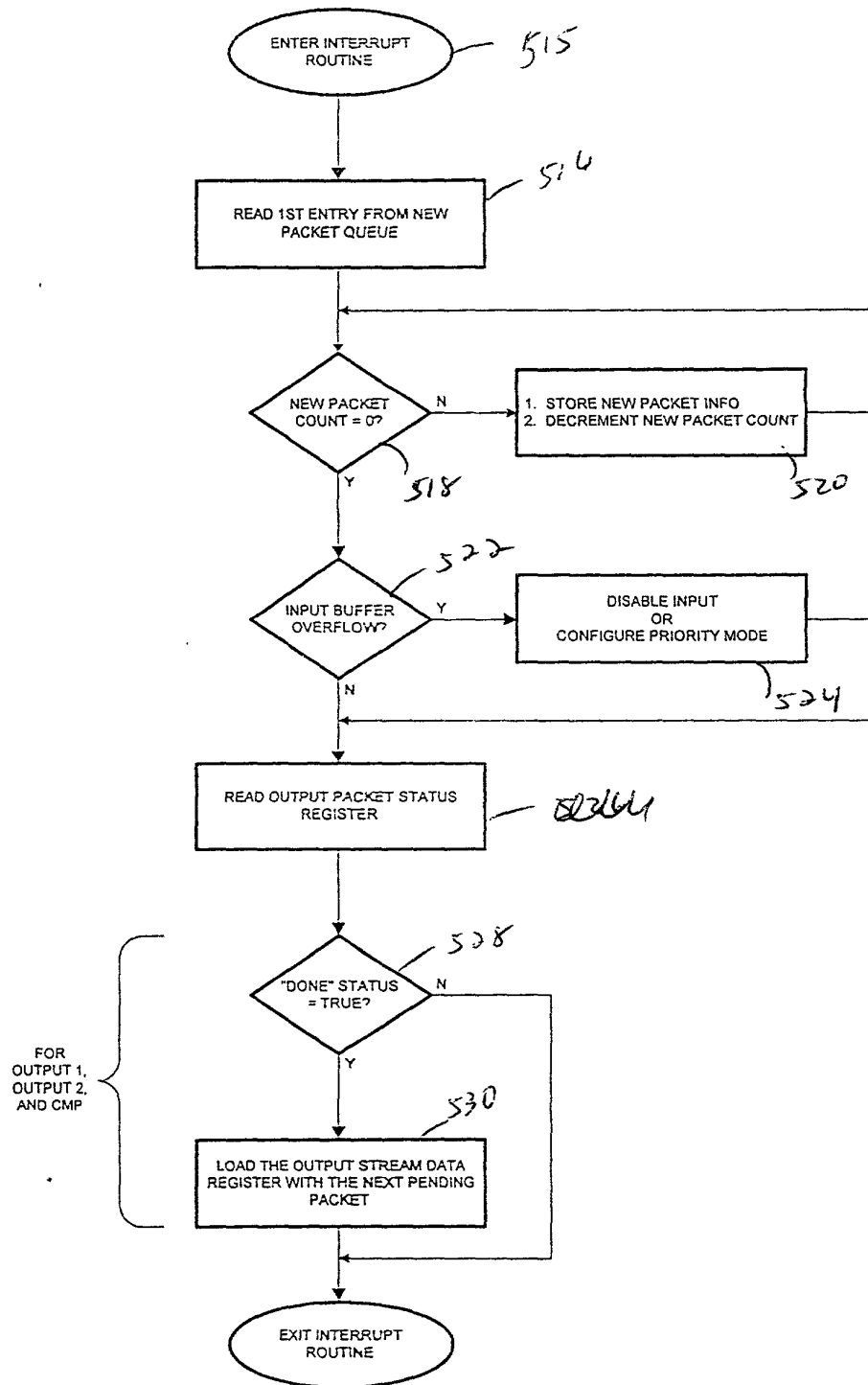


FIG. 5